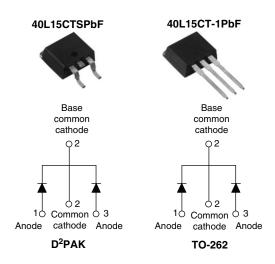


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 20 A				
V _R	15 V			
I _{RM} 600 mA at 100 °C				

FEATURES

- 125 °C T_J operation ($V_R < 5 V$)
- · Center tap module
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for Q101 level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	A		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	700	A		
V _F	19 Apk, $T_J = 125 \ ^{\circ}C$ (per leg, typical)	0.25	V		
TJ		- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	40L15CTSPbF 40L15CT-1PbF	UNITS
Maximum DC reverse voltage	V _R	T _{.1} = 100 °C	15	N/
Maximum working peak reverse voltage	V _{RWM}	1j = 100 C	15	v

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per leg		50 % duty cycle at T_{C} = 85 °C, rectangular waveform		20	
forward current	per device	IF(AV)			40	_
Maximum peak one cycle non-repetitive surge current per leg			5 µs sine or 3 µs rect. pulse	Following any rated load	700	- A -
		IFSM	10 ms sine or 6 ms rect. pulse	condition and with rated	330	
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 2 \text{ A}, L = 6 \text{ mH}$			10	mJ		
Repetitive avalanche current p	oer leg	I _{AR}	$ \begin{tabular}{lllllllllllllllllllllllllllllllllll$		А	

* Pb containing terminations are not RoHS compliant, exemptions may apply



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
		19 A	T _J = 25 °C	-	0.41	V
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A		-	0.52	
See fig. 1	V FM (")	19 A	T _J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current per leg	I _{BM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	V _R = Rated V _R	-	10	mA
See fig. 2	IRM \''	$T_J = 100 \ ^{\circ}C$		-	600	mA
Threshold voltage	V _{F(TO)}	- T _J = T _J maximum		0.1	182	V
Forward slope resistance	r _t			7	.6	mΩ
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8 -		nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µ		V/µs		

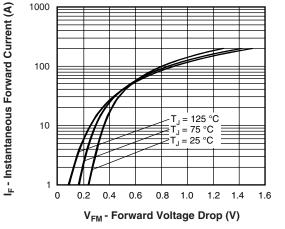
Note

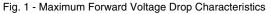
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

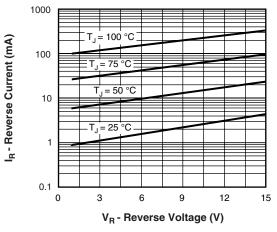
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature	range	TJ		- 55 to 125	- °C
Maximum storage temperature	range	T _{Stg}		- 55 to 150	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	1.5	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	40	
Approvimeto weight				2	g
Approximate weight				0.07	oz.
Mounting torque minimum maximum			Non-lubricated threads	6 (5)	kgf · cm
			Non-lubricated threads	12 (10)	(lbf · in)
Marking device			Case style D ² PAK	40L15CTS	8
			Case style TO-262	40L15CT-	1

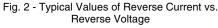


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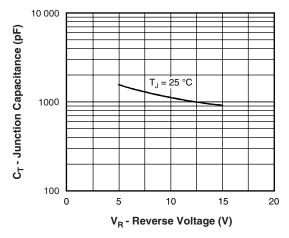


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

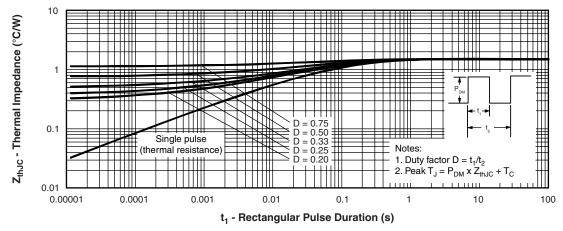
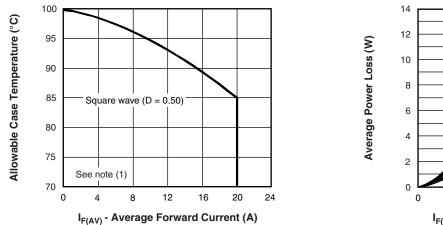
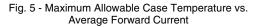
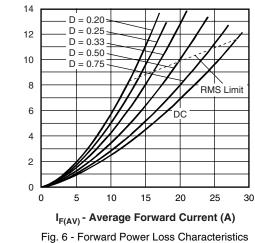


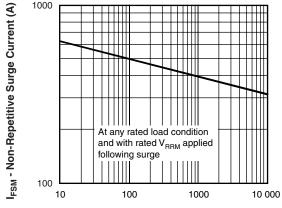
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

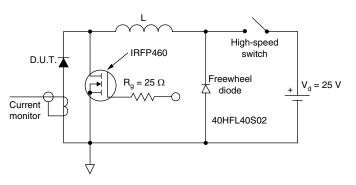


Fig. 8Unclamped Inductive Test Circuit

Note

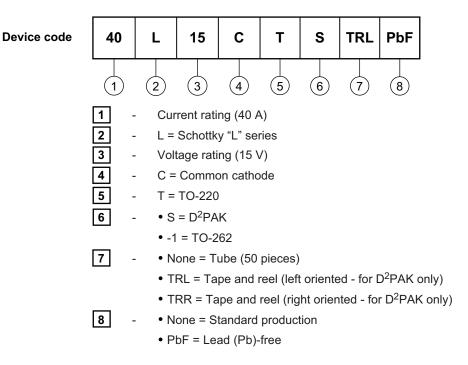
(1)

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?9501			
Part marking information	http://www.vishay.com/doc?95008		
Packaging information	http://www.vishay.com/doc?95032		



Vishay

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